


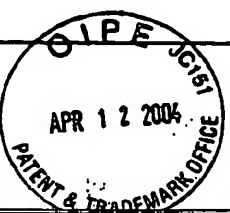


ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention	LOW VOLTAGE CIRCUIT FOR INTERFACING WITH HIGH VOLTAGE ANALOG SIGNALS																												
<p>Application Number: 10/671200 </p> <p>Confirmation Number: 6943</p> <p>First Named Applicant: CHRISTIAN Dupuy et al.</p> <p>Attorney Docket Number: ATM-240</p> <p>Art Unit: 2816</p> <p>Search string: (5402064 or 5198700).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>PLN</td><td>1</td><td>5402064</td><td>1995-03-28</td><td>Robert E. Eck et al</td><td>A1</td><td>324</td><td>207.21</td></tr><tr><td>PLN</td><td>2</td><td>5198700</td><td>1993-03-30</td><td>Frank A. Whiteside</td><td>A1</td><td>307</td><td>273</td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>HAI L. NGUYEN</td><td>07/19/2004</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	PLN	1	5402064	1995-03-28	Robert E. Eck et al	A1	324	207.21	PLN	2	5198700	1993-03-30	Frank A. Whiteside	A1	307	273	Examiner Name	Date	HAI L. NGUYEN	07/19/2004
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FORM PTO-1449			Atty. Docket No. ATM-240		Serial No. 10/671,200	
LIST OF PRIOR ART CITED BY APPLICANT			Applicants: Christian Dupuy et al.			
			Filing Date: Sept. 25, 2003		Group: 2816	



U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Publ. Date	Name	Class	Sub Class	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						

FOREIGN PATENT DOCUMENTS						
Examiner Initial*	Document Number	Publ. Date	Country	Class	Sub Class	Translation Yes No
HA AK	0 565 806	10/20/93	EPO	—	—	✓
HA AL	0 998 022	5/3/00	EPO	—	—	✓
AM						
AN						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
AO	
AP	
AQ	

EXAMINER: <i>HAI L. NGUYEN</i>	DATE CONSIDERED: <i>09/19/2004</i>
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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Title of
Invention

LOW VOLTAGE CIRCUIT FOR INTERFACING WITH HIGH
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Application Number: 10/671200



Confirmation Number: 6943

First Named Applicant: Christian Dupuy

Attorney Docket Number: ATM-240

Art Unit: 2816

Search string: (3946303 or 5227714 or 5287071 or 5519656
or 5686821 or 5694035 or 5698972 or 5801556
or 5889393 or 6188211 or 6304131 or 6448750
or 20020070762).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
HA	1	3946303	1976-03-23	Streit et al.	A1	323	22 T
	2	5227714	1993-07-13	Lou	A1	323	282
	3	5287071	1994-02-15	Olmstead et al.	A1	330	258
	4	5519656	1996-05-21	Maccarrone et al.	A1	365	189.09
	5	5686821	1997-11-11	Brokaw	A1	323	273
	6	5694035	1997-12-02	Keeth	A1	323	349
	7	5698972	1997-12-16	Keeth	A1	323	349
	8	5801556	1998-09-01	LeFevre	A1	327	103
	9	5889393	1999-03-30	Wrathall	A1	323	282
	10	6188211	2001-02-13	Rincon-Mora et al.	A1	323	280
	11	6304131	2001-10-16	Huggins et al.	A1	327	538
▼	12	6448750	2002-09-10	Shor et al.	A1	323	282

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

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init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
<i>HL</i>	1	20020070762	2002-06-13	You et al.	A1	327	55

Signature

Examiner Name	Date
<i>HAI L. NGUYEN</i>	<i>09/19/2004</i>



FORM PTO-1449			Atty. Docket No. ATM-240		Serial No. 10/671,200	
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	AH					
	AI					
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
<i>HN</i>	AJ	Data sheet: Intersil, "Buck and Synchronous-Rectifier (PWM) Controller and Output Voltage Monitor", HIP6004E, October 2001, File No. 4997.1, pp. 1-12.				
<i>HN</i>	AK	Data sheet no. PD94251: International Rectifier, "Dual Synchronous PWM Controller with Current Sharing Circuitry and LDO Controller", IRU3046, Sept. 12, 2002, Rev. 1.8, pp. 1-20.				
<i>HN</i>	AL	Data sheet: Linear Technology, "3-Phase, 5-Bit VID, 600kHz, Synchronous Buck Switching Regulator Controller, FES LTC3732, May 2002, pp. 1-28.				
<i>HN</i>	AM	Data sheet: STMicroelectronics, "5-Bit Programmable Dual-Phase Controller", L6917B, Sept. 2002, pp. 1-33.				
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	BH					
	BI					
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
HZN	BJ	Data sheet: ON Semiconductor, "Two-Phase Buck Controller with Integrated Gate Drivers for VRM 9.0", PON CS5332, May 2001, Rev. 10, pp. 1-20.				
HZN	BK	Data sheet: Maxim Integrated Products, "VRM 9.0, Dual-Phase, Parallelable, Average Current-Mode Controller", MAX5037, July 2002, Rev. 0, pp. 1-30.				
HZN	BL	Website printout: Maxim Integrated Products, "DC-DC Converter Tutorial", Oct. 19, 2000, pp. 1-9.				
	BM					
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